Notice of References Cited Application/Control No. 10/052,999 Examiner Andre R. Fowlkes Applicant(s)/Patent Under Reexamination TIRUMALAI ET AL. Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	Α	US-6,961,930	11-2005	Waldspurger et al.	717/141
	В	US-			
	С	US-			
	D	US-	-		
	E	US-			
	F	US-			
	G	US-			
	Н	US-			
	ı	US-			
	J	US-			
	К	US-			
	L	US-			
	М	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	0					
	Р					
	Q	,				
	R					
	s			-		
	Т					

NON-PATENT DOCUMENTS

*	-	Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)			
	U	P. Tirumalai, et al., Processor Aware Anticipatory Prefetching in Loops, 2/14/04, Sun Microsystems, Inc., pp. 1-10.			
	V	T. Mowry, et al., Design and evaluation of a compiler algorithm for prefetching. 9/1992. Proceedings of the 5th International Conference on Architectural Support for Programming Languages and Operating Systems. pp 63-73.			
	w	CK. Luk, et al., Compiler-based prefetching for recursive data structures. 10/1996. Proceedings of the 7 th International Conference on Architectural Support for Programming Languages and Operating Systems. pp. 222-234.			
	×.	CK. Luk and T. Mowry. Architectural and compiler support for effective instruction prefetching: A cooperative approach. ACM Transactions on Computer Systems 2/2001 (TOCS), Volume 19, Issue 1. pp. 71-109.			

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.